

Figure 1a

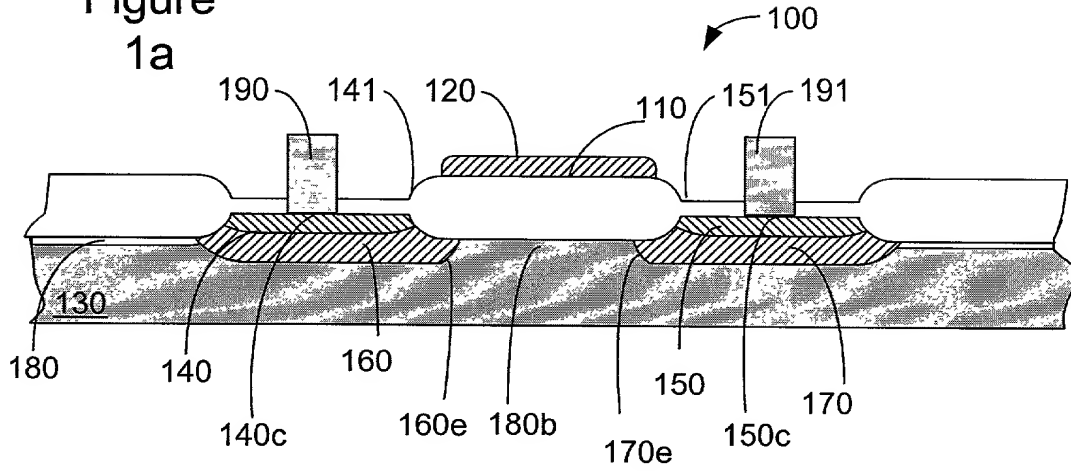
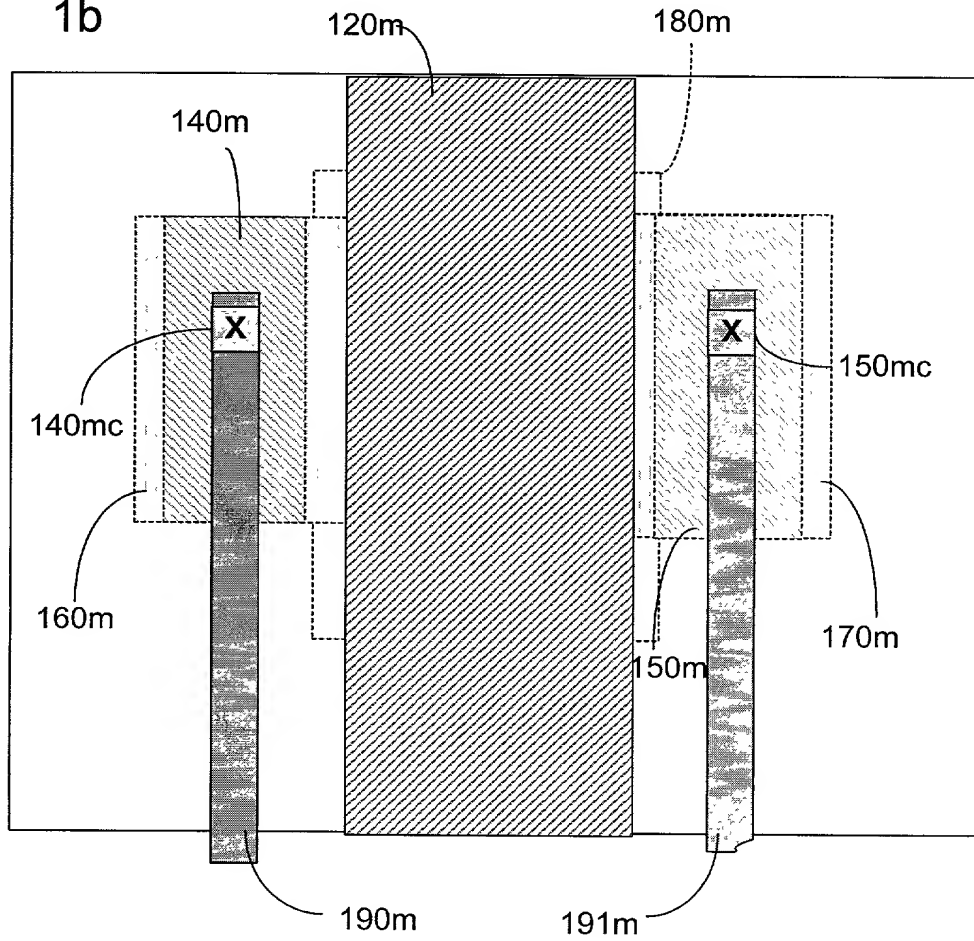


Figure 1b



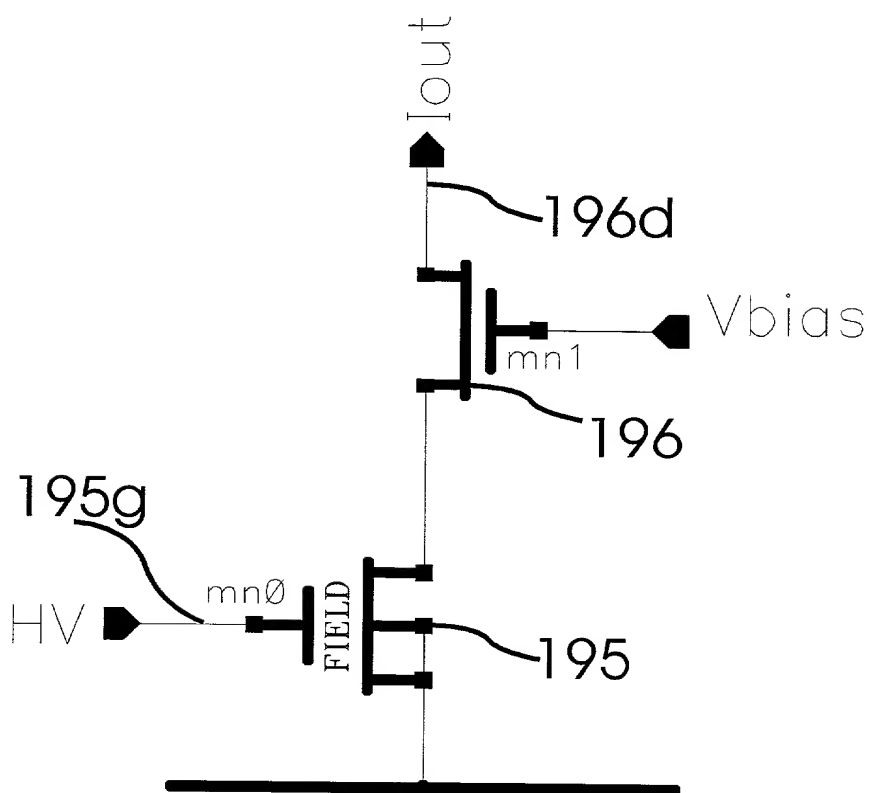


Figure 2a

Title: HIGH VOLTAGE INTEGRATED CIRCUIT AMPLIFIER  
 Applicant: Mark A. Lemkin Docket: AIMI-01841US0  
 Appl. No.: Unknown Atty: Larry E. Vierra  
 Filing Date: August 31, 2001 Phone: (415) 369-9660  
 Express Mail No.: EL 901 895 821 US

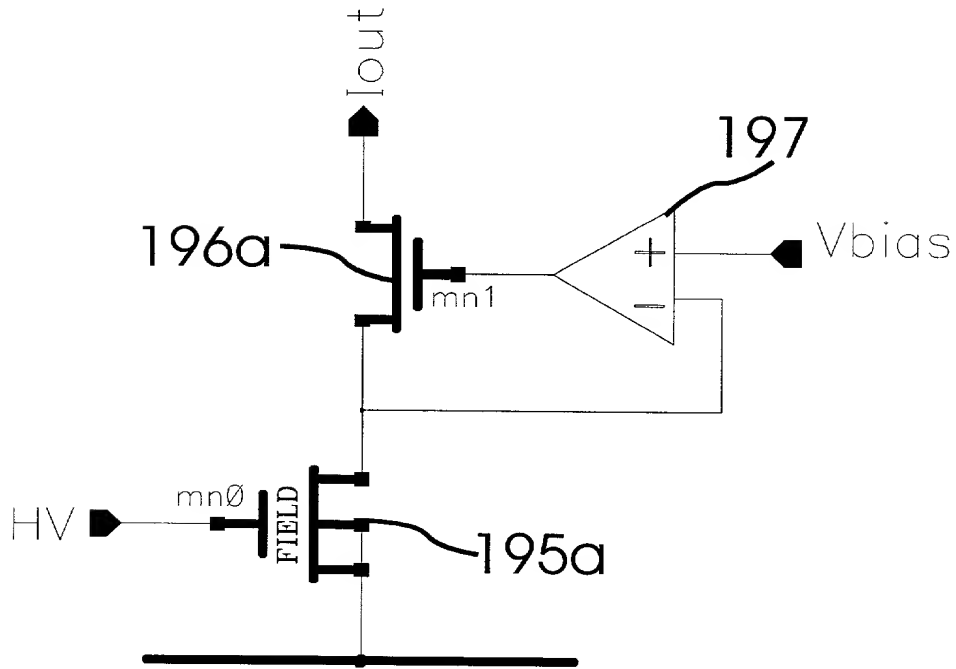
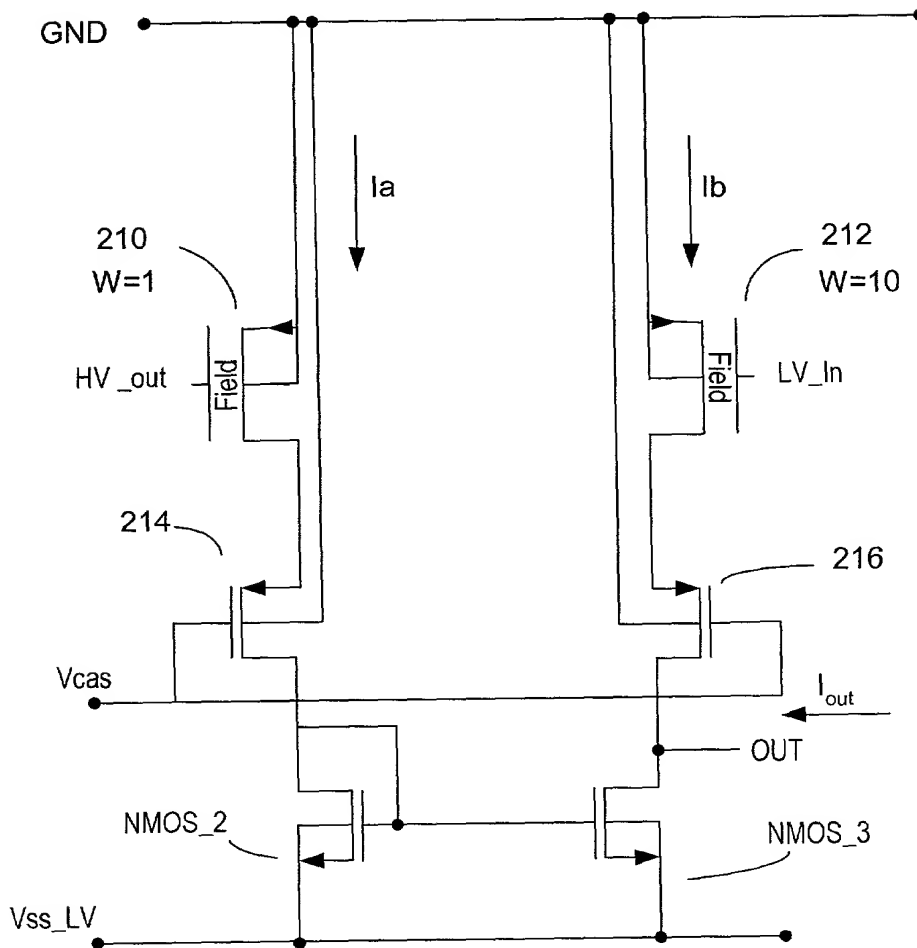


Figure 2b

FIG. 2B is a schematic diagram of the circuit of FIG. 2A.

FIGURE 3

200



Title: HIGH VOLTAGE INTEGRATED CIRCUIT AMPLIFIER  
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Applicant. Mark A. Lemkin

Docket: AIMI-01841US0

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Atty: Larry E. Vierra

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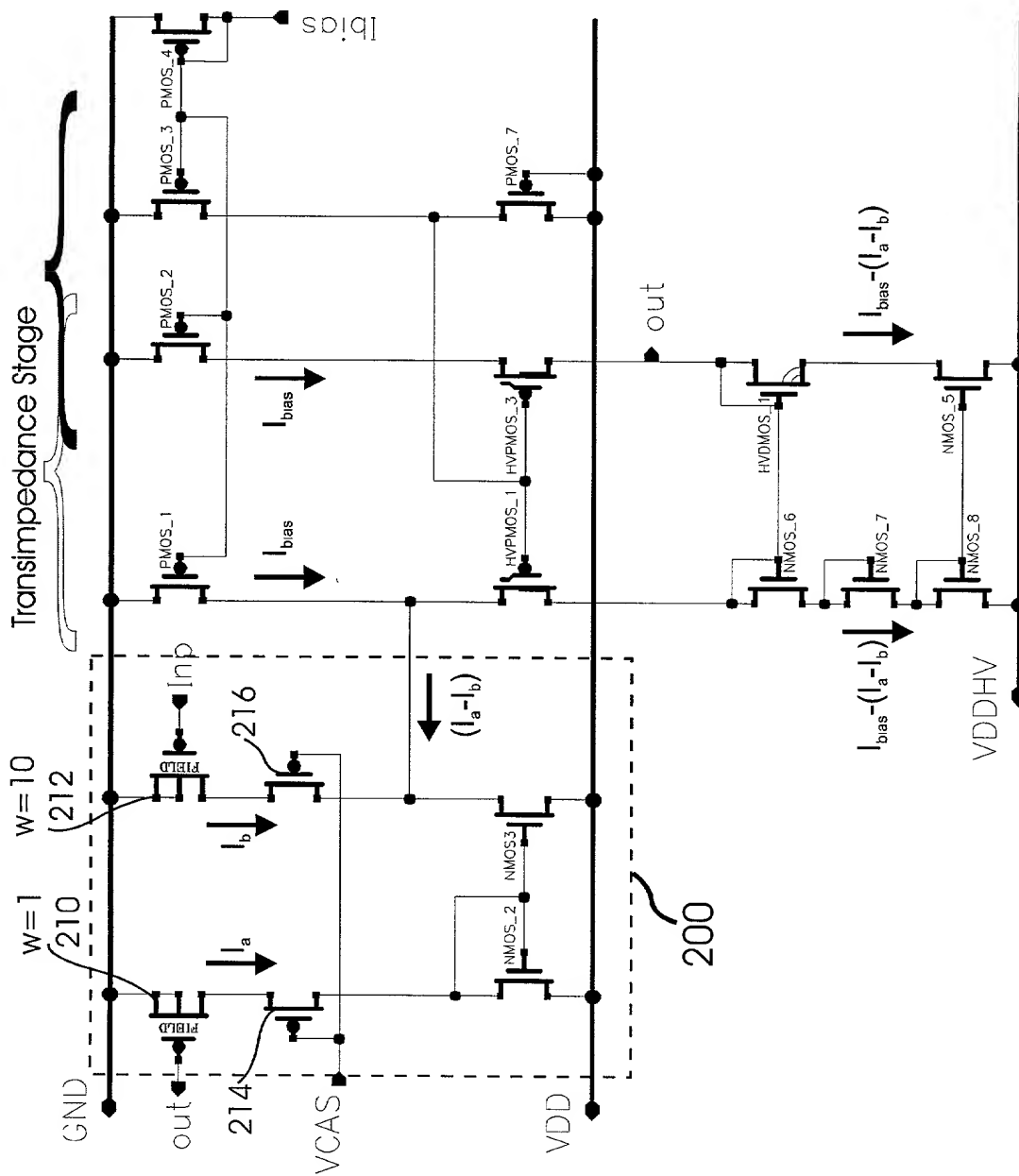


Figure 4a

Title: HIGH VOLTAGE INTEGRATED CIRCUIT AMPLIFIER  
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 Appl. No.: Unknown Atty: Larry E. Vicerra  
 Filing Date: August 31, 2001 Phone: (415) 369-9660  
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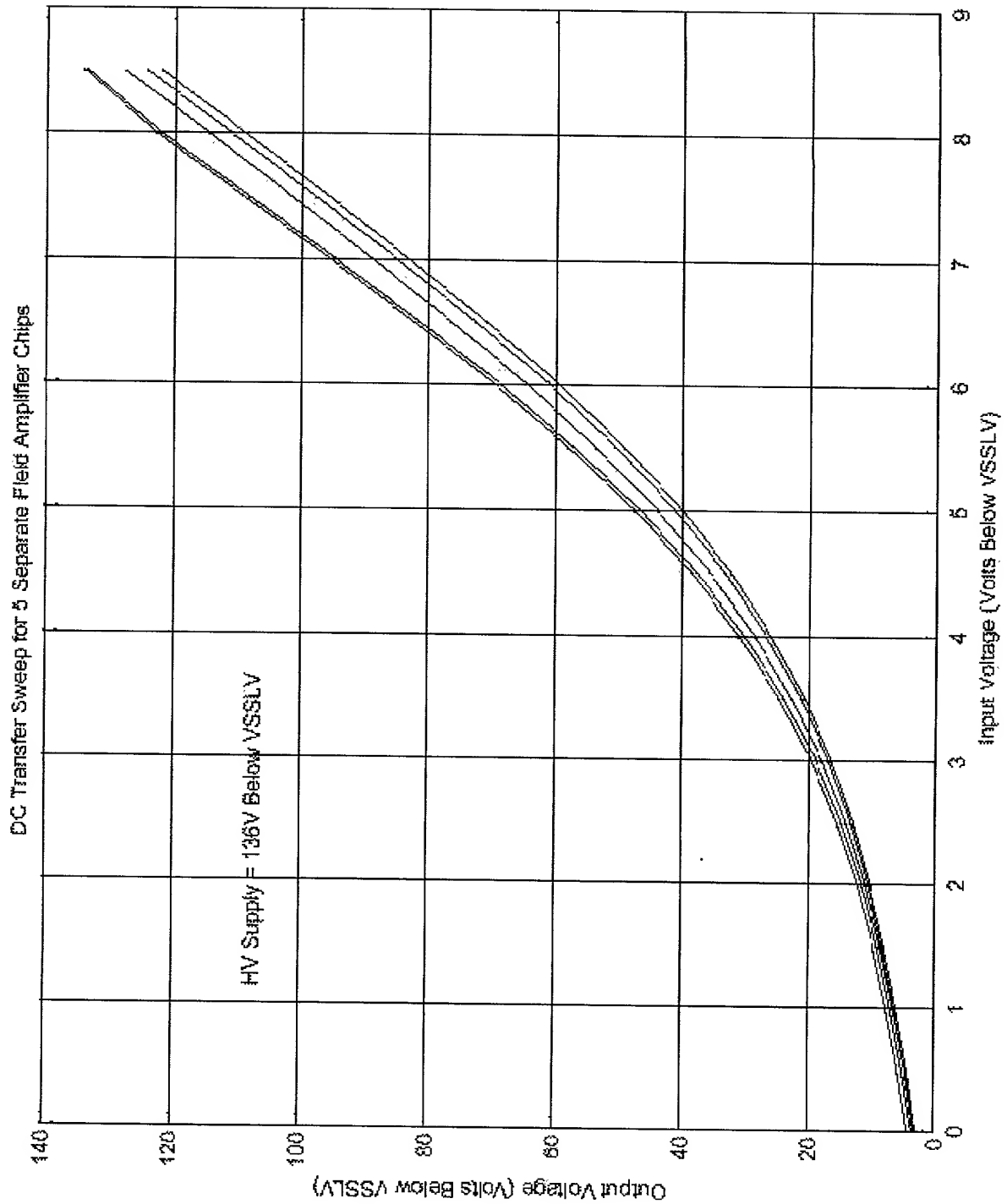


Figure 4b

FOIEBQ 06344660

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Express Mail No.: EL 901 895 821 US

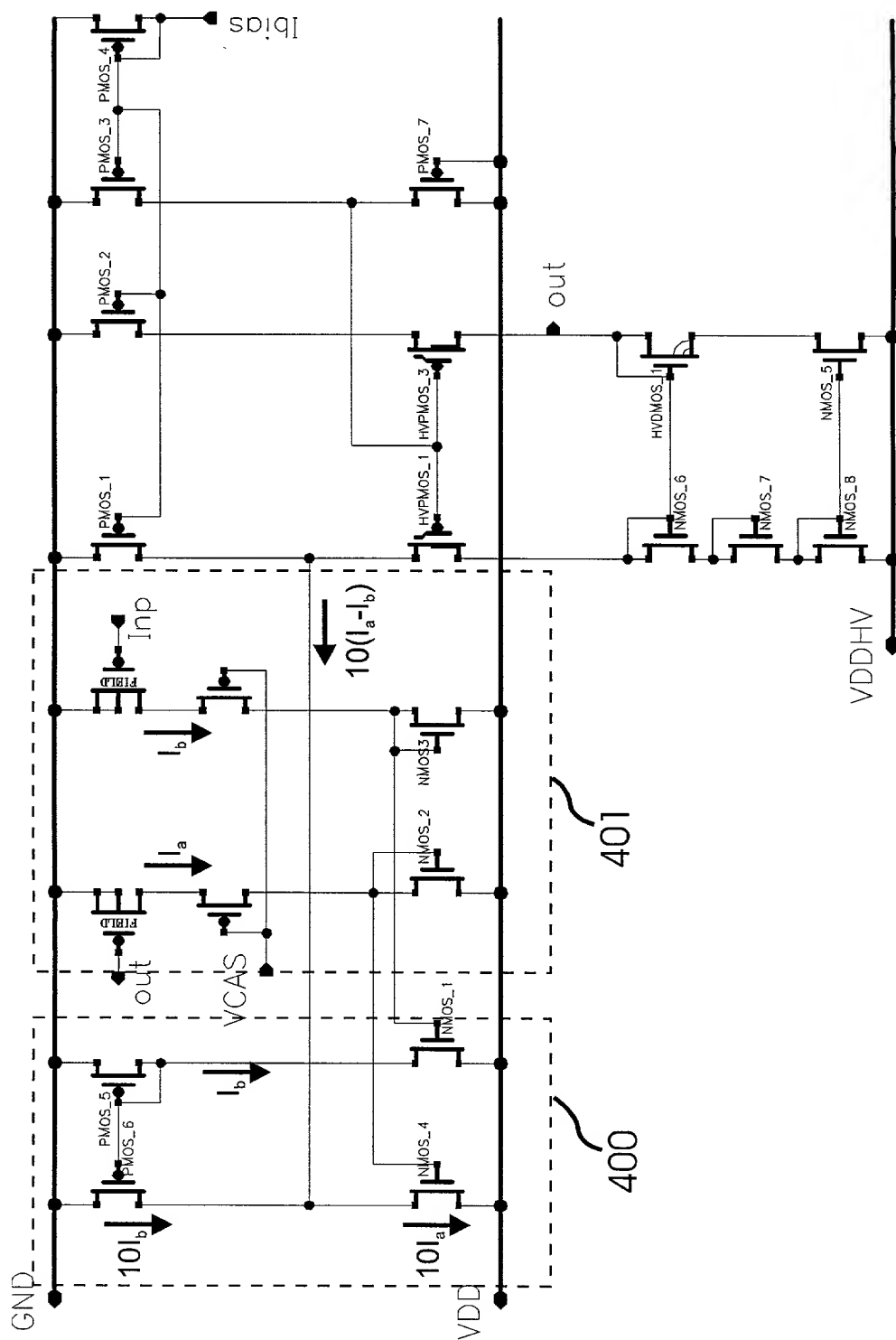


Figure 5

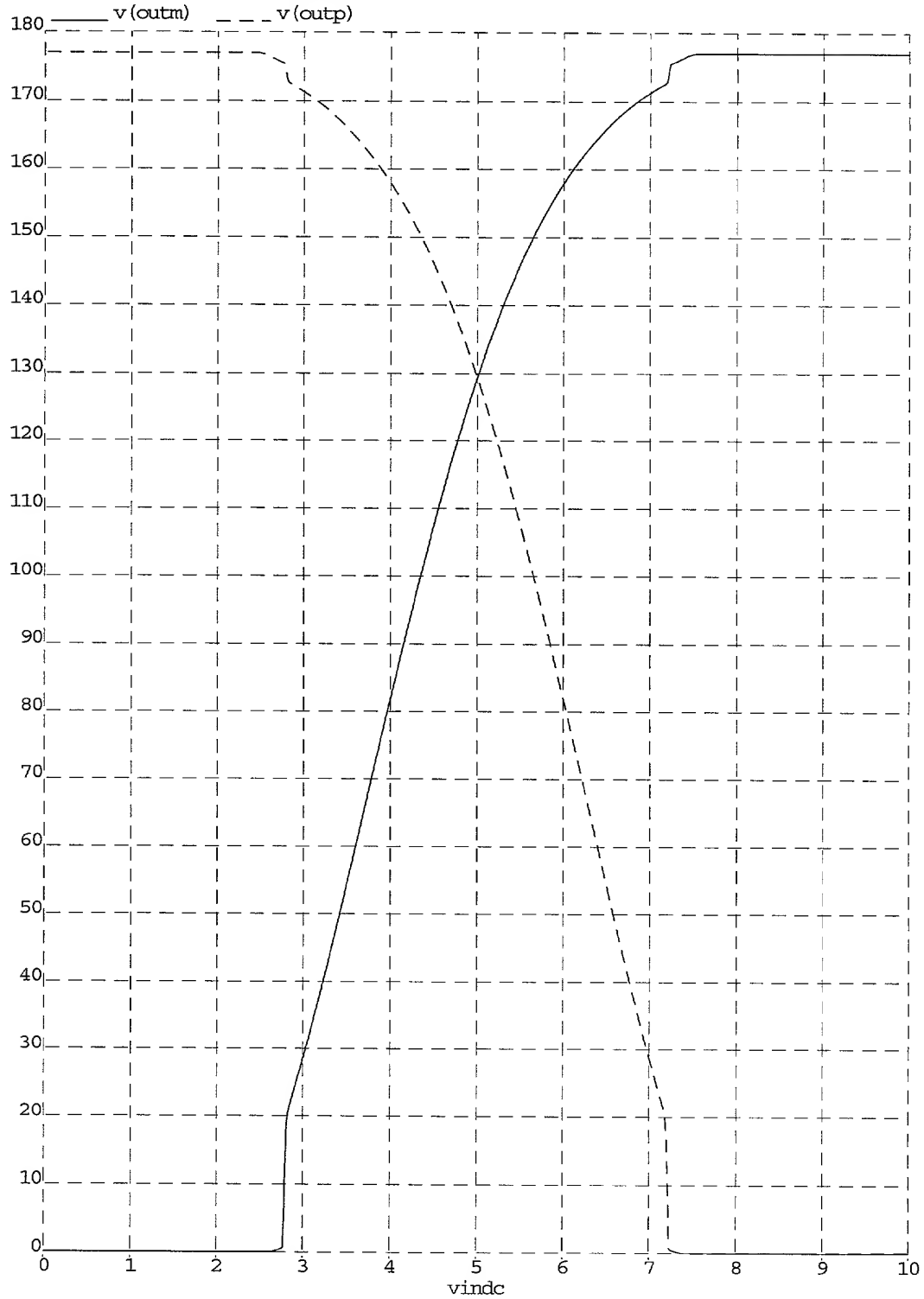




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Figure 7

Vcm = 130, HVDD = 200V, HVVSS=190, HVCAS=195, VDD=10V, VSS=0V, 10p Load each side



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### Figure 8

V<sub>in</sub>=4.5, V<sub>AREF</sub>=5.5, V<sub>cm</sub> Varies, H<sub>VDD</sub> = 200V, H<sub>VSS</sub>=190, H<sub>V</sub>CAS=195, V<sub>DD</sub>=10V, V<sub>SS</sub>=0V, 10p Load each side

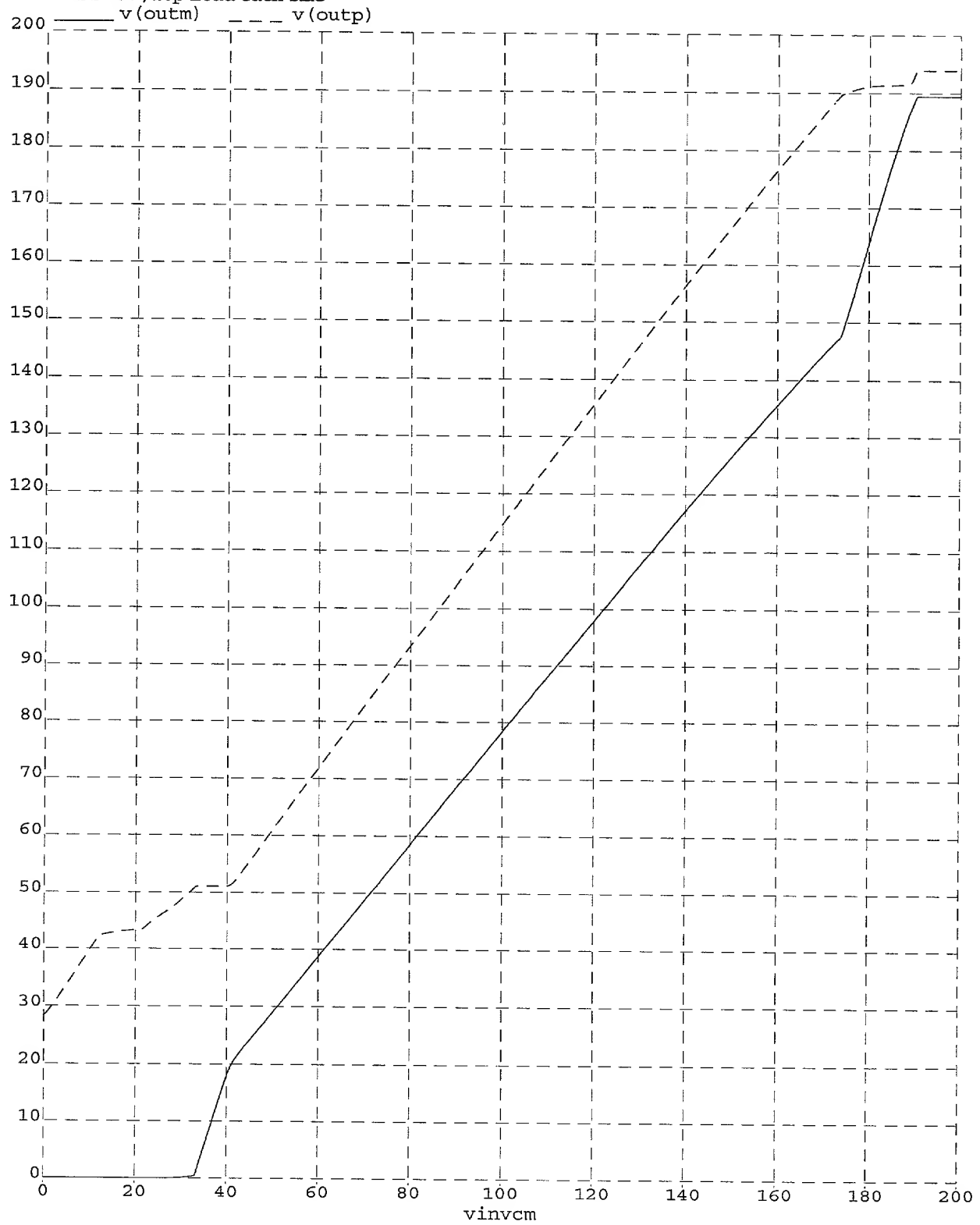
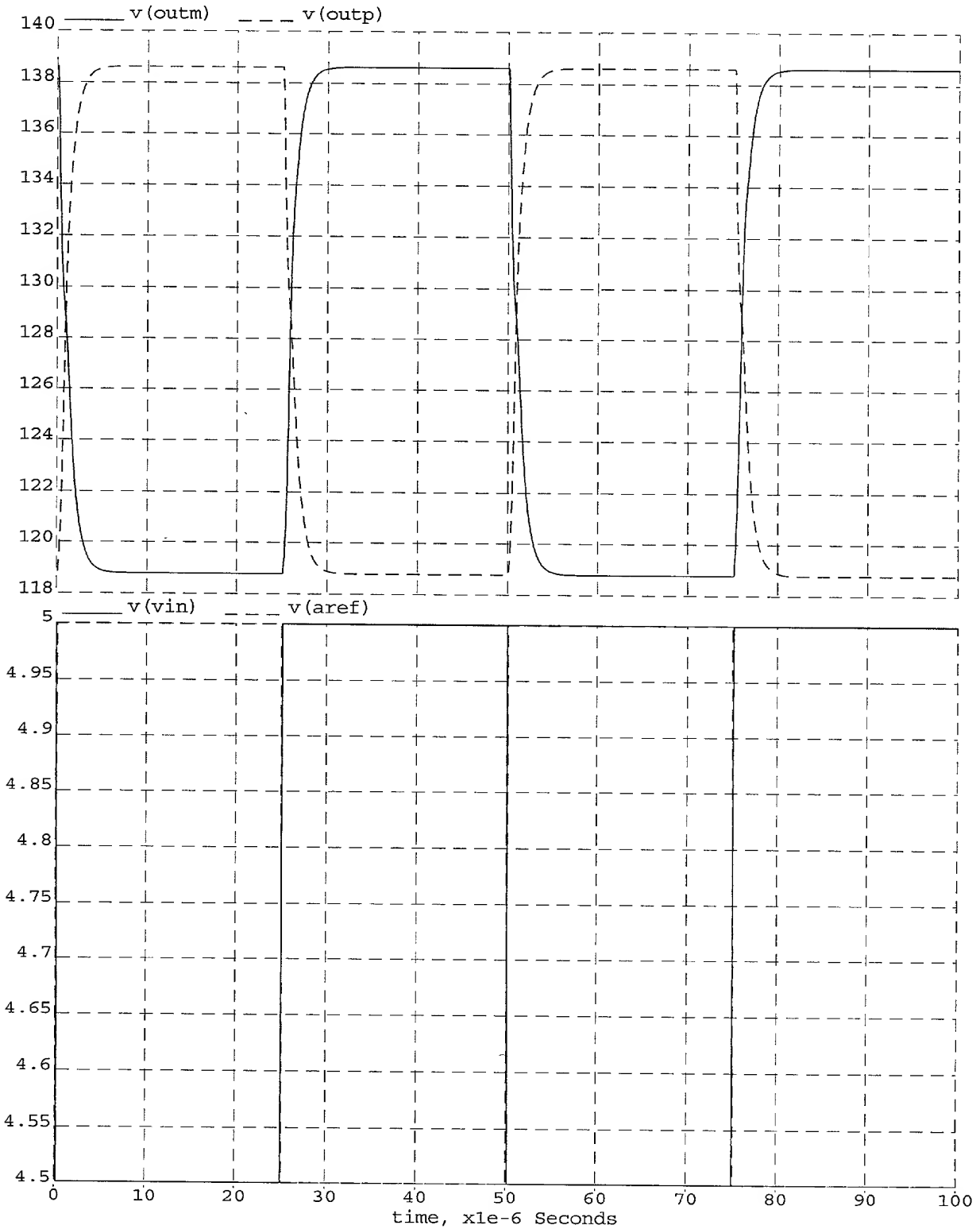


Figure 9

$V_{cm} = 130$ ,  $HVDD = 200V$ ,  $HVVSS = 190$ ,  $HVCAS = 195$ ,  $VDD = 10V$ ,  $VSS = 0V$ ,  
 10p Load each side



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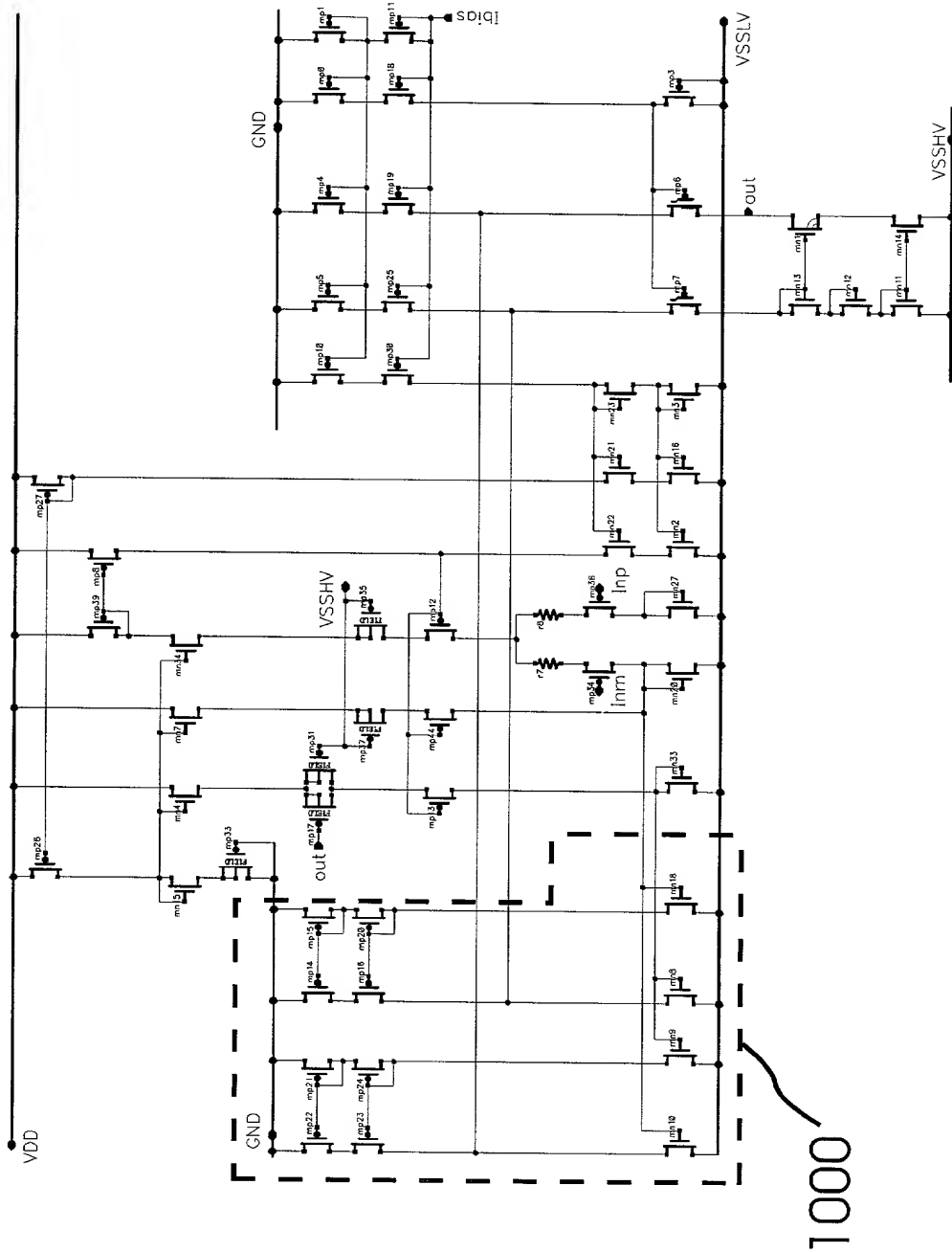


Figure 10